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2816

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Keisuke GOTO and Sachiko EDO

Appln. No.: 09/826,179

: Art Unit: 2816

Filed: April 4, 2001

: Examiner: LE, DINH THANH

For: DLL CIRCUIT AND METHOD OF
GENERATING TIMING SIGNALS

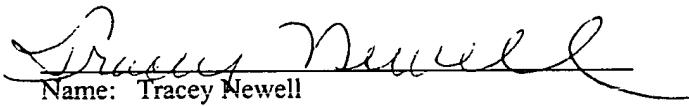
: Docket No.: KPM-01501

#3/Action
7/29/02
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Certificate of Mailing

I hereby certify that the foregoing documents are being deposited with the United States Postal Service as first class mail, in an envelope addressed to the Commissioner for Patents Washington, D.C. 20231 on this date of July 8, 2002.


Name: Tracey Newell

RESPONSE TO RESTRICTION REQUIREMENT

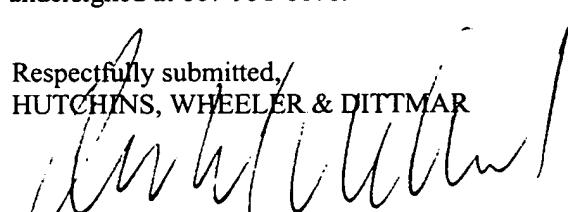
Commissioner for Patents
Washington, DC 20231

Sir:

In response to the Office Action dated June 10, 2002, in which restriction was required, Applicant elects Group II, claims 1-9, 17-23.

Favorable consideration and allowance are earnestly solicited. Should there be any questions after reviewing this paper, the Examiner is invited to contact the undersigned at 617-951-6676.

Respectfully submitted,
HUTCHINS, WHEELER & DITTMAR


Donald W. Muirhead
Reg. No. 33,978

July 8, 2002
Date

Patent Group
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